

FIG.1

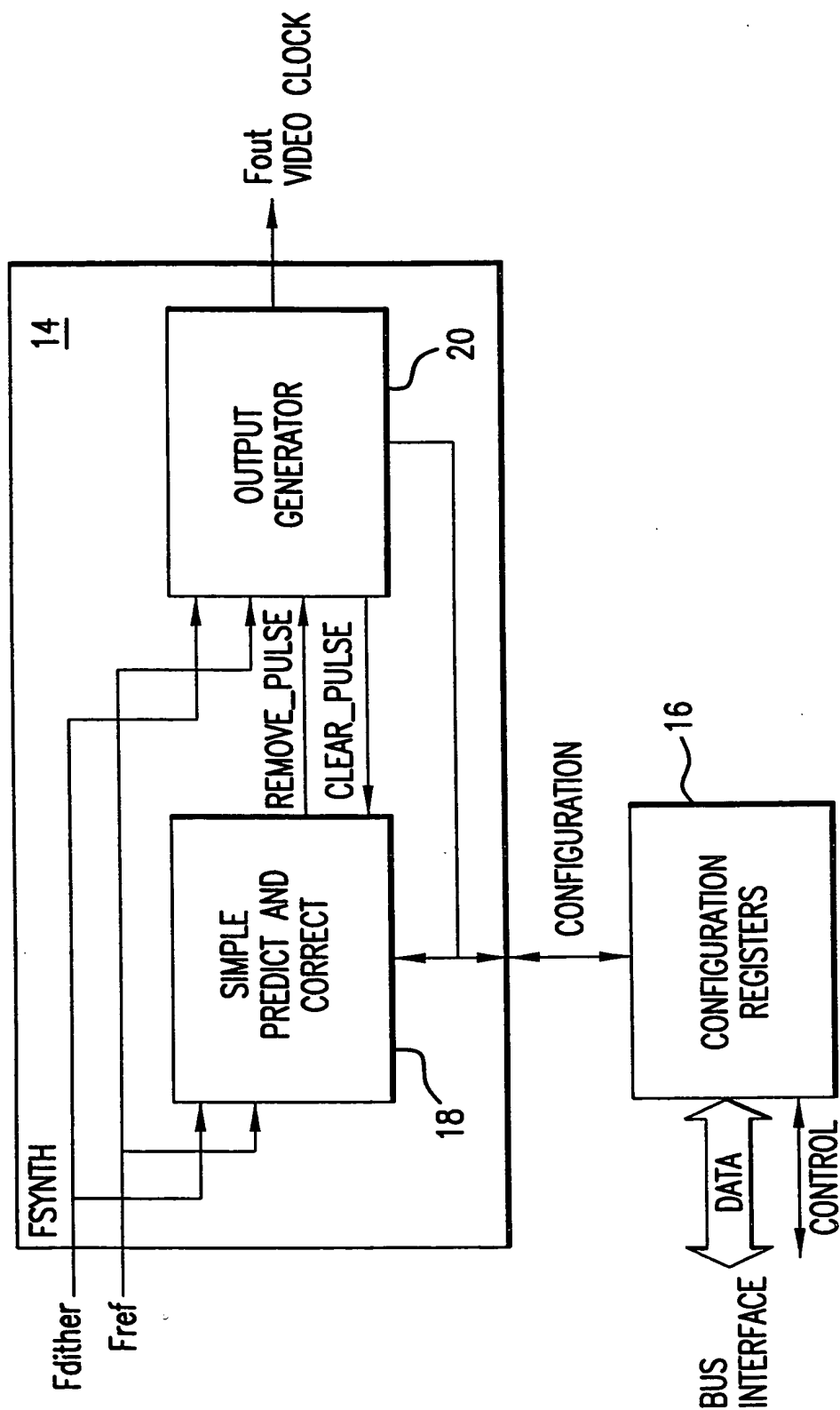
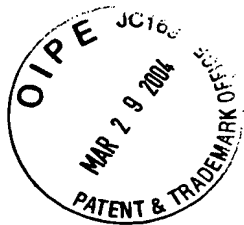


FIG. 2

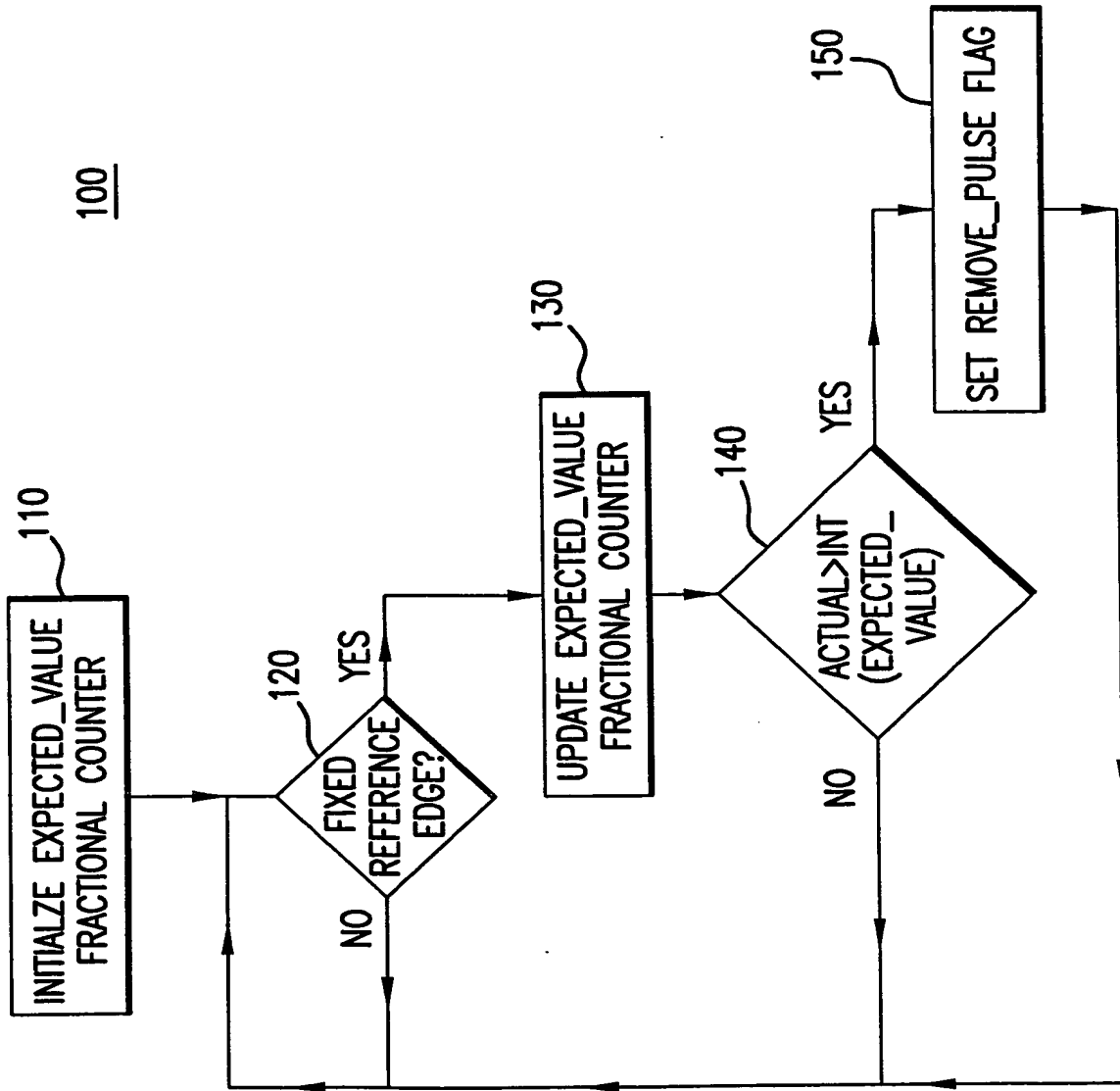
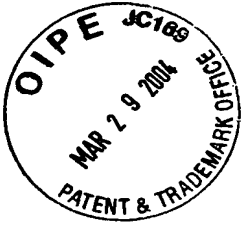


FIG.3

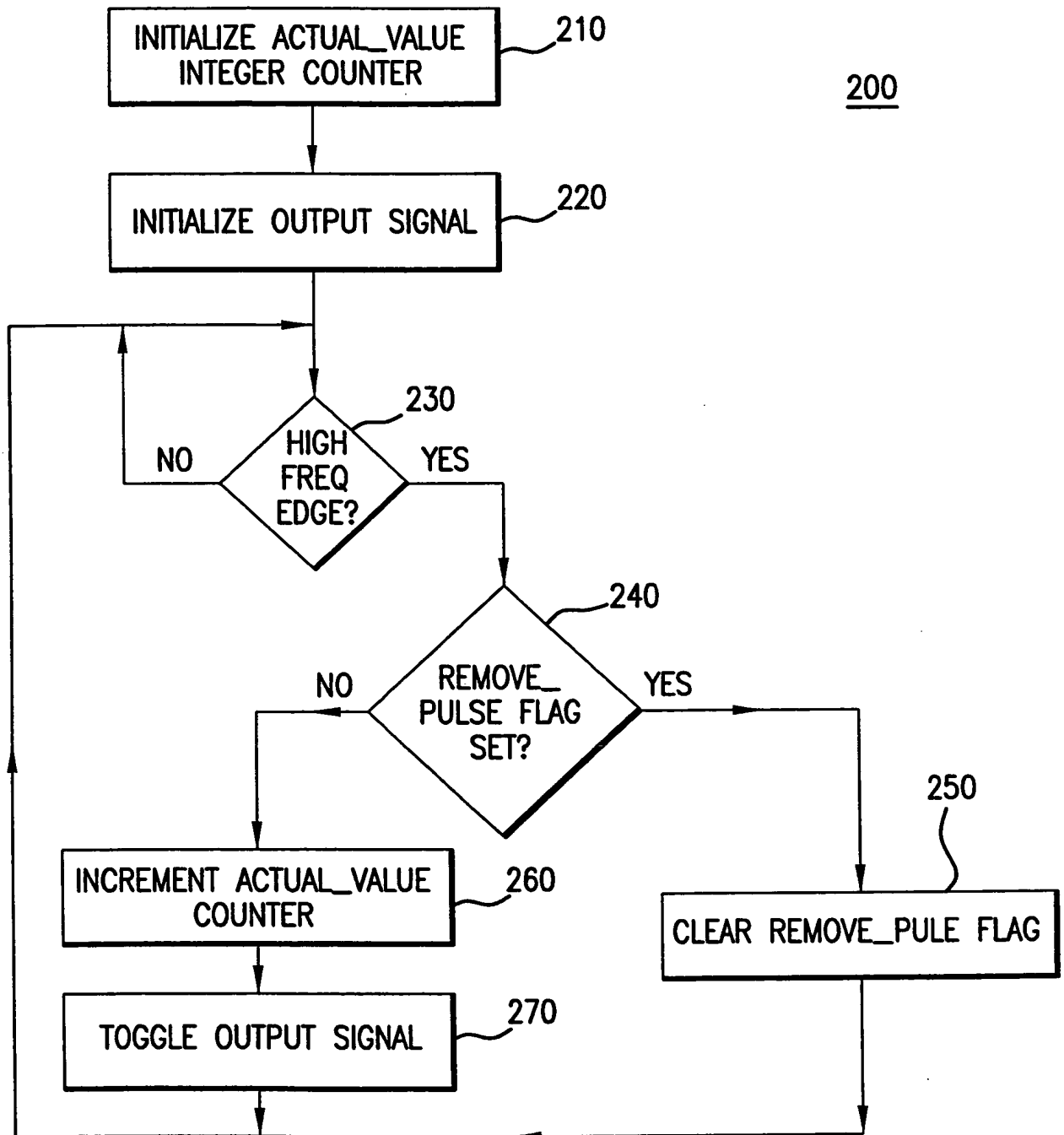
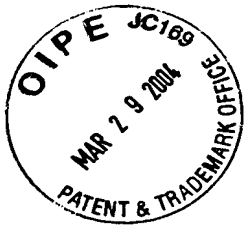
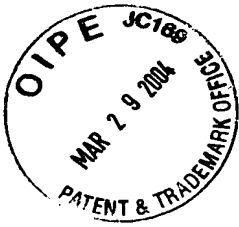
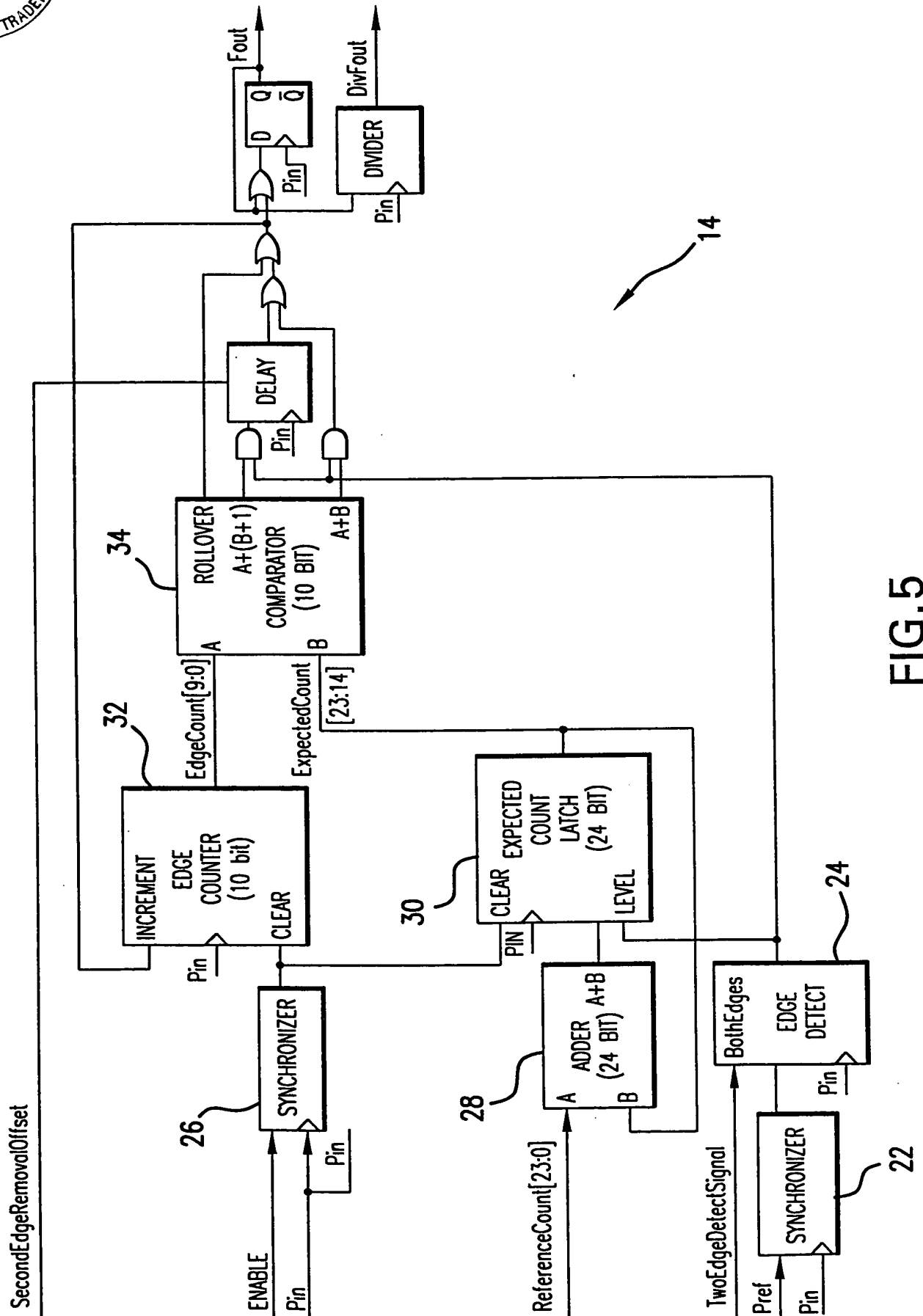


FIG.4



Docket No.: 10030825-1
Serial No.: 10/696,811
Inventor(s): Keithley, et al
Title: Fixed Frequency Clock Output Having a Variable High Frequency
Input Clock and an Unrelated Fixed Frequency Reference Signal



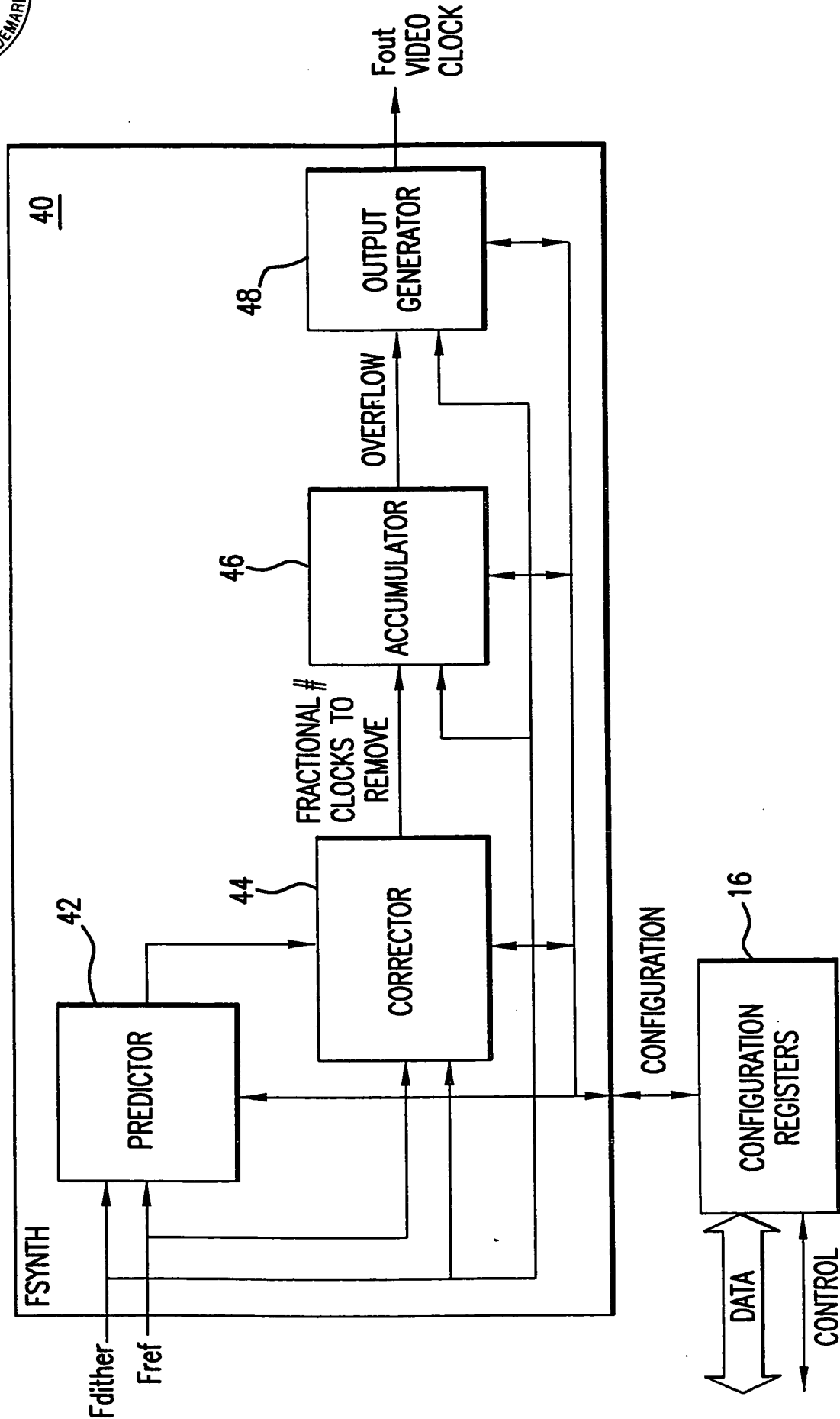
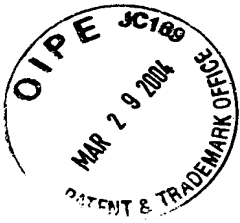


FIG. 6

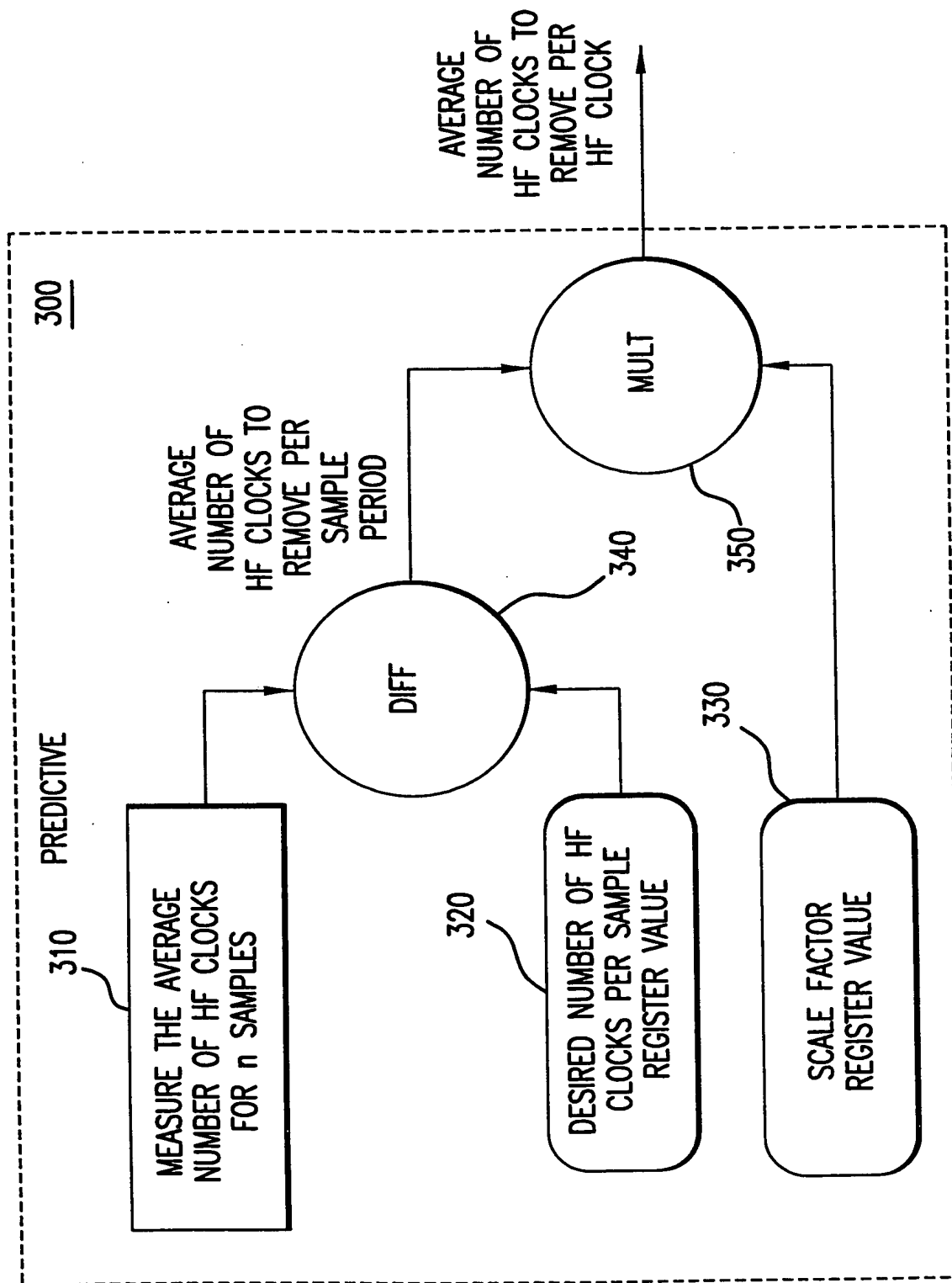
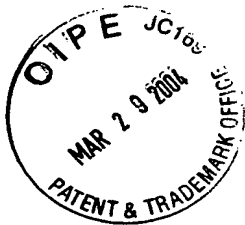


FIG.7

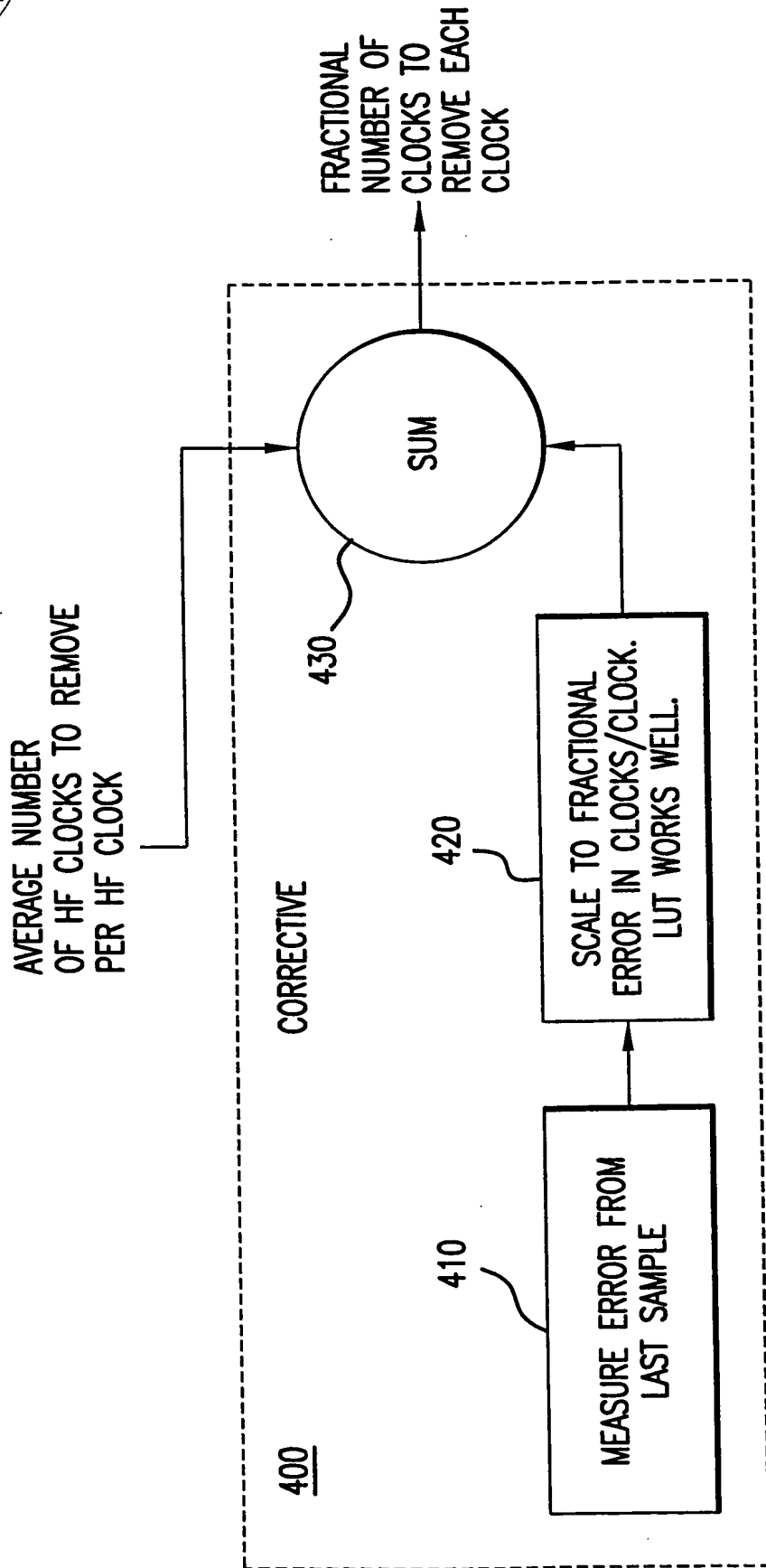


FIG.8